

ABSTRACT OF THE DISCLOSURE

1 In a TDMA/TDD transmitter, a control data generation unit receives
2 assignment terms for a number of time slots and slot data from an external
3 source, produces a set of assignment control data according to the assignment
4 terms and the slot data and stores the set of assignment control data into an
5 entry of a control data table in response to a transfer command signal. A
6 sequence controller analyzes sets of assignment control data maintained in
7 the control data generation unit and produces a number of address pointers.
8 The address pointers are stored in an address pointer table in such a sequence
9 that they can be sequentially read out in a desired transmission sequence.
10 The aforesaid transfer command signal is supplied from the sequence
11 controller to the control data generation unit in response to each of the
12 address pointers. A framing unit sequentially reads address pointers from
13 the starting address of the address pointer table, reads assignment control
14 data from the control data table in accordance with the read address pointers,
15 and formulates a frame. Transmit data stored in a data memory is inserted
16 into the frame for transmission.